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Notice of Allowability	Application No.	Applicant(s)
	10/701,278	CHOU, HSIEN-YING
	Examiner	Art Unit
	David Vu	2828
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. 🗵 This communication is responsive to <u>applicant's filing date of 11/03/2003</u> .		
2. ☑ The allowed claim(s) is/are <u>1 and 2</u> <b>-3</b>		
3. X The drawings filed on 03 November 2003 are accepted by the Examiner.		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). <ul> <li>a)</li></ul></li></ul>		
<ul> <li>Attachment(s)</li> <li>1.  Notice of References Cited (PTO-892)</li> <li>2.  Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3.  Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date</li></ul>	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☑ Examiner's Amendn	e

## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows: claim 2, line 2, "after the coupling step" has been deleted.

2. The following is an examiner's statement of reasons for allowance: the prior art does not teach or fairly suggest a driving method for a liquid crystal display comprising, in combination with the remaining features of the claims, changing a gate voltage of the transistor to drive the transistor; during a display period of a frame, for each pixel, applying a display voltage to a liquid crystal unit and changing the display voltage of the liquid crystal unit to a blanking display voltage of a black frame by changing the gate voltage of the transistor.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Vu whose telephone number is (571) 272-1831. The examiner can normally be reached on M-F 8am-430pm.

The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

David Vu Primary Examiner Art Unit 2828